

FIG. 1 is a schematic diagram of a device 10, showing a substrate 20, a layer 30, and a layer 40. The layer 30 is composed of a plurality of particles 32. The layer 40 is a thin layer on top of the layer 30. The device 10 is shown in cross-section, with a top surface 50 and a bottom surface 60. The layer 30 is shown in a perspective view, with the particles 32 arranged in a regular pattern. The layer 40 is shown in a perspective view, with the thin layer 40 covering the top surface of the layer 30. The device 10 is shown in a perspective view, with the substrate 20, the layer 30, and the layer 40. The device 10 is shown in a perspective view, with the substrate 20, the layer 30, and the layer 40.

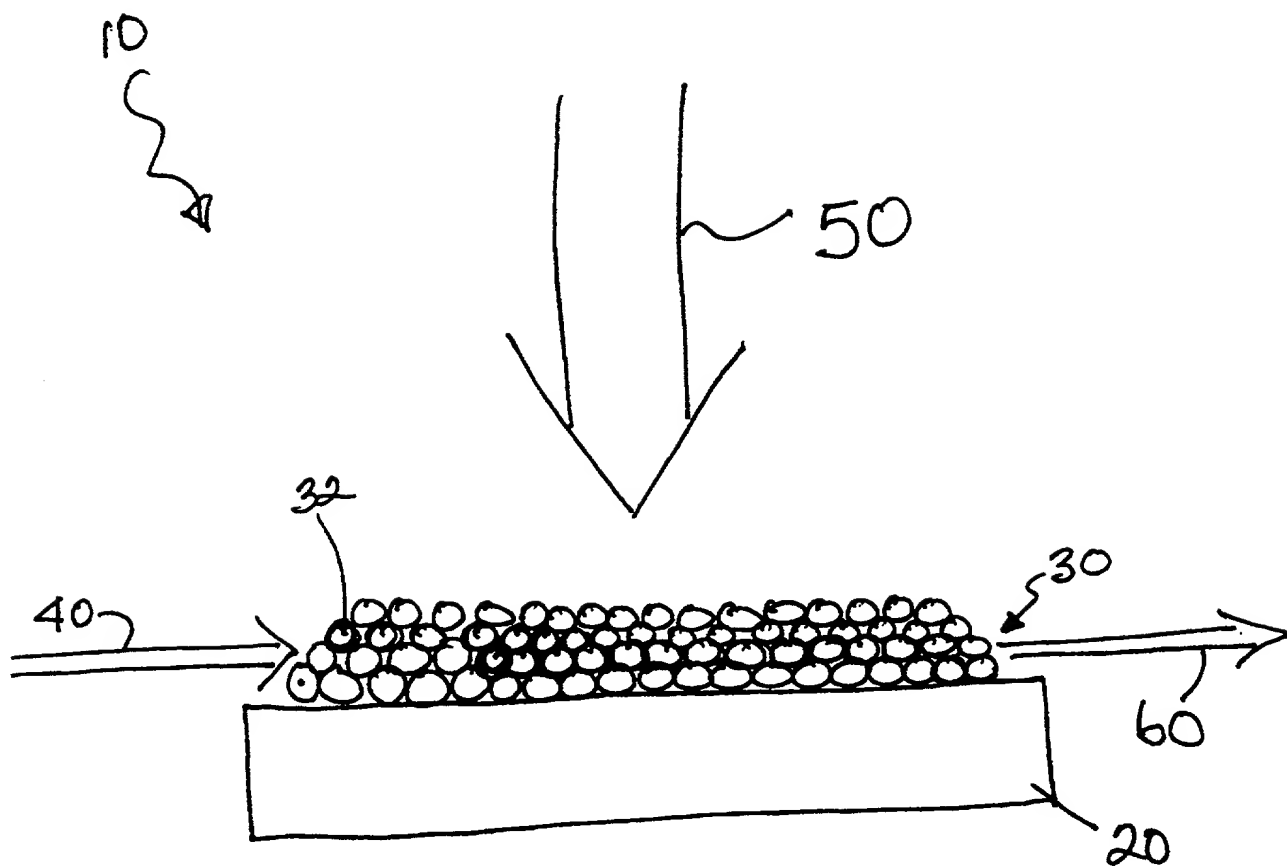


FIG 1

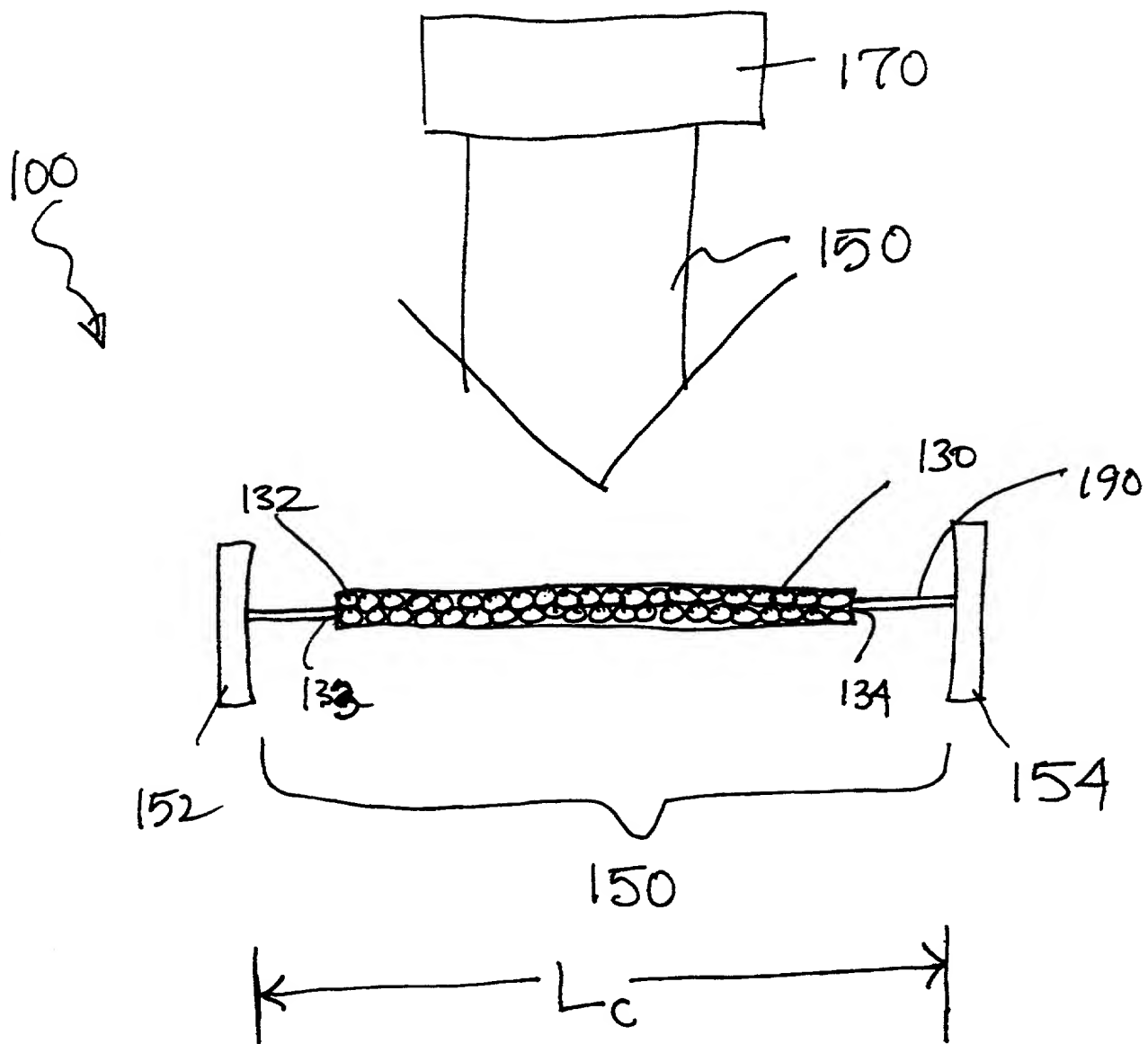
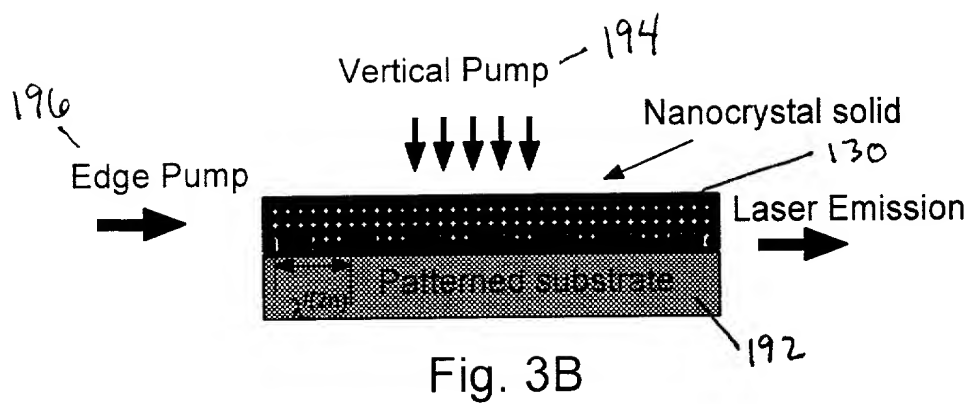
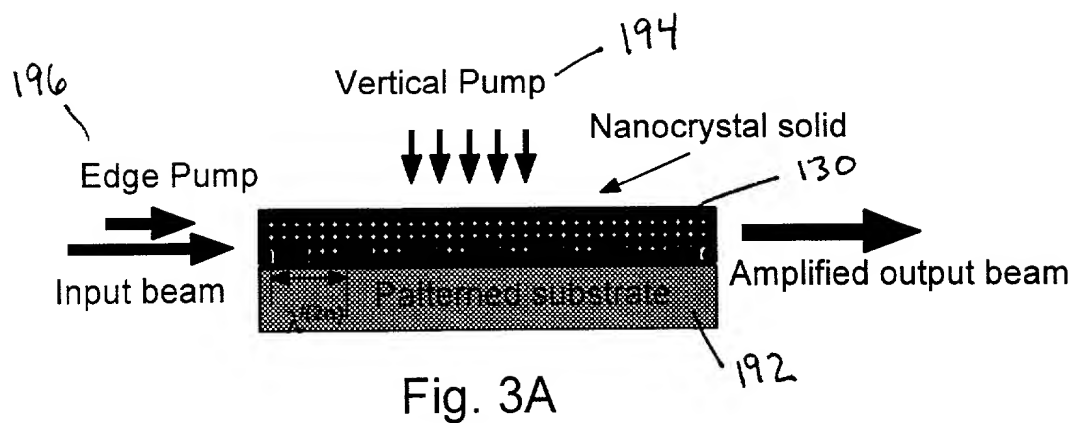


Fig 2



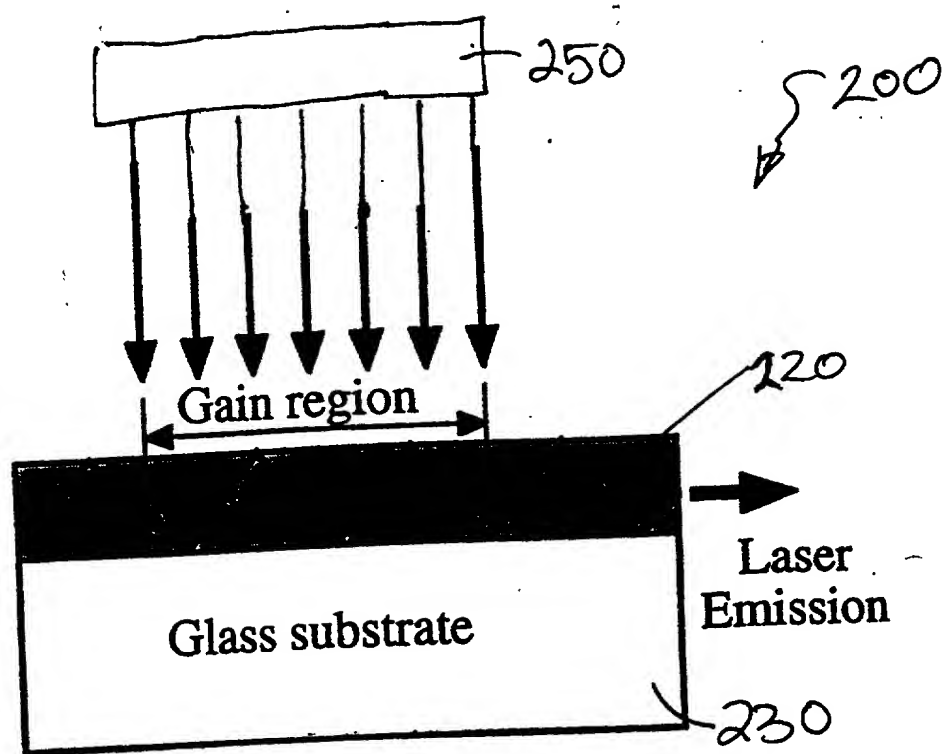


Fig. 4

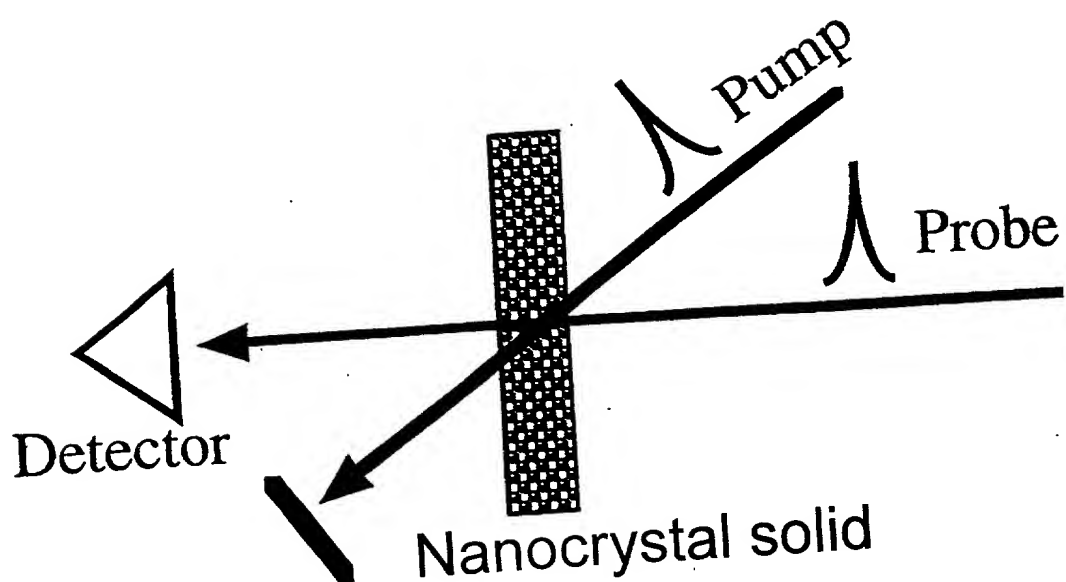
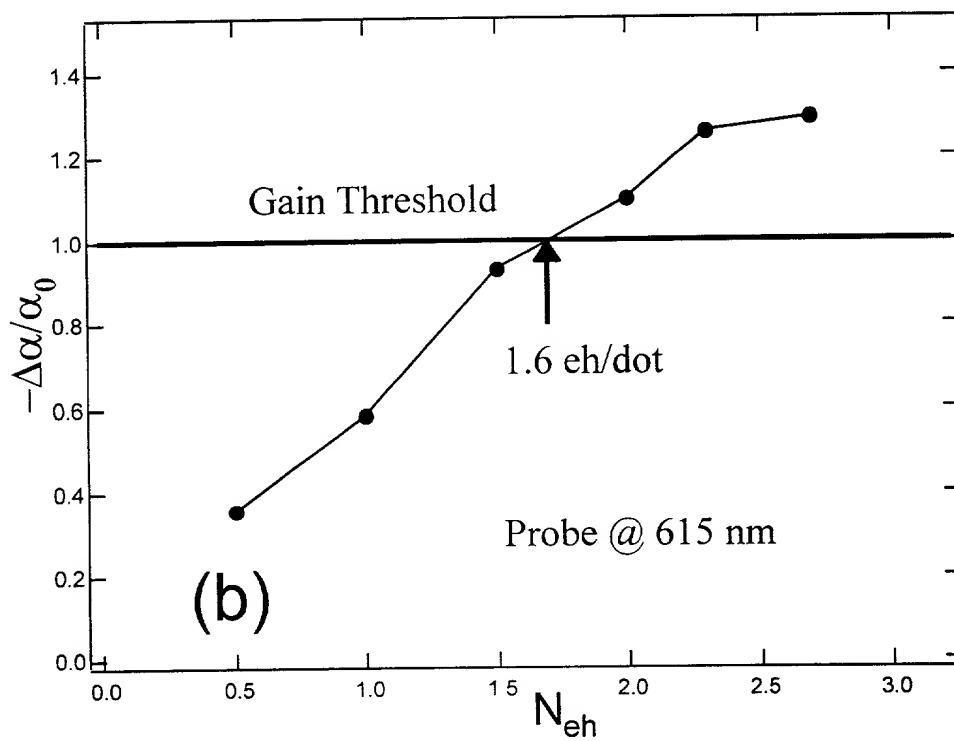
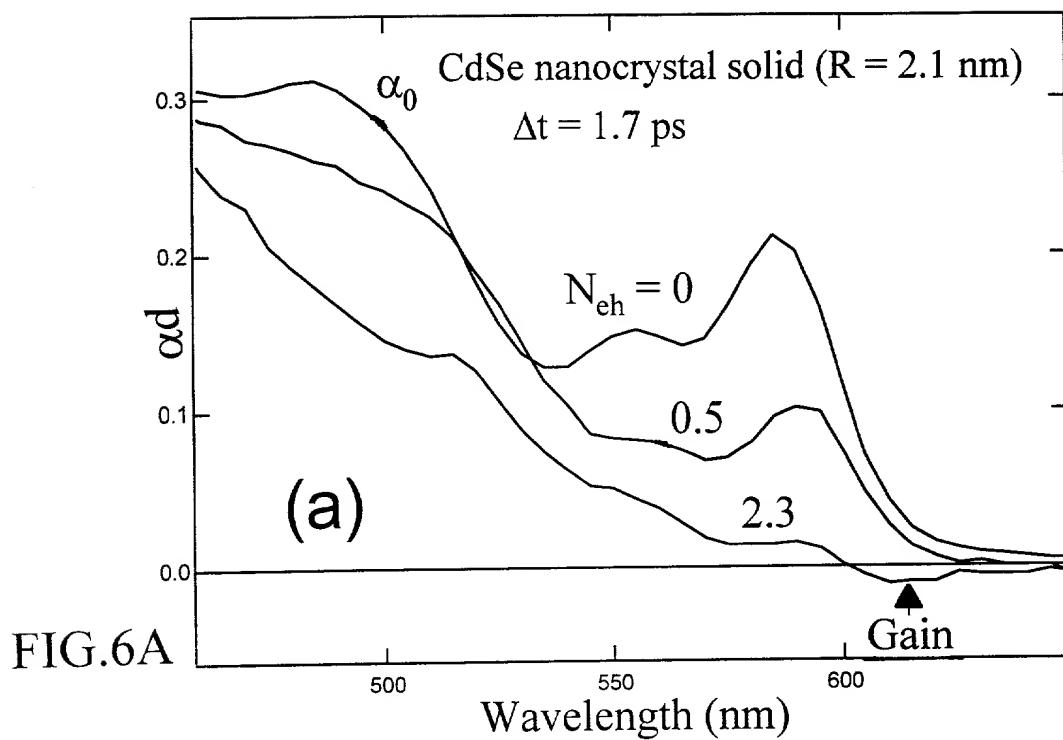


FIG. 5



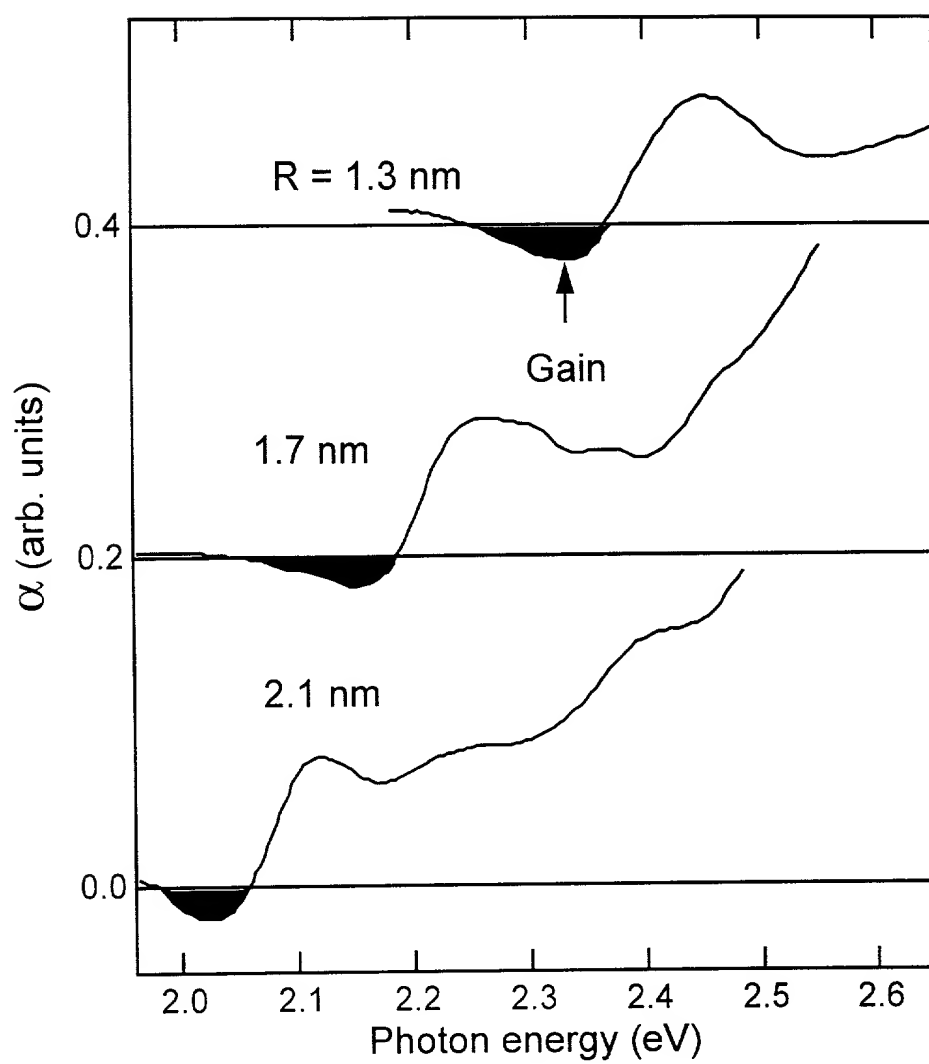


FIG. 7

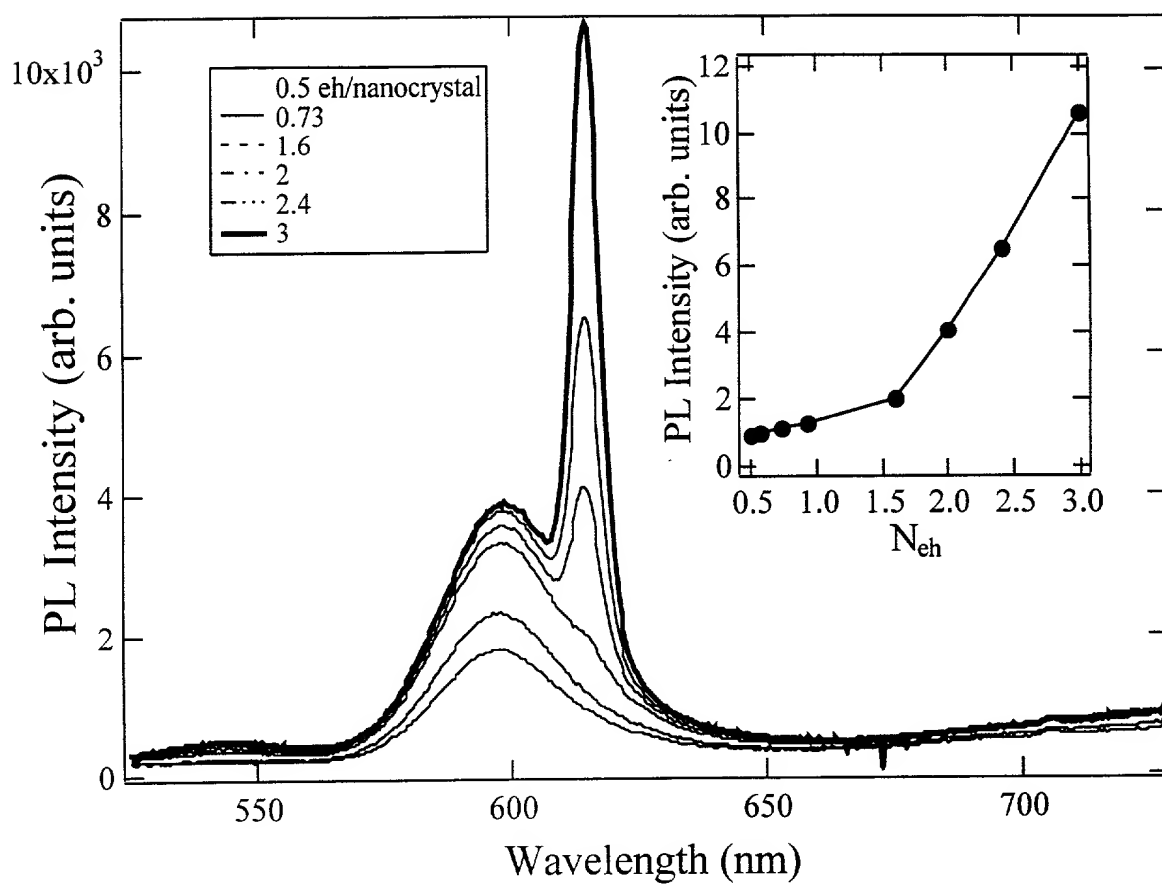


FIG. 8



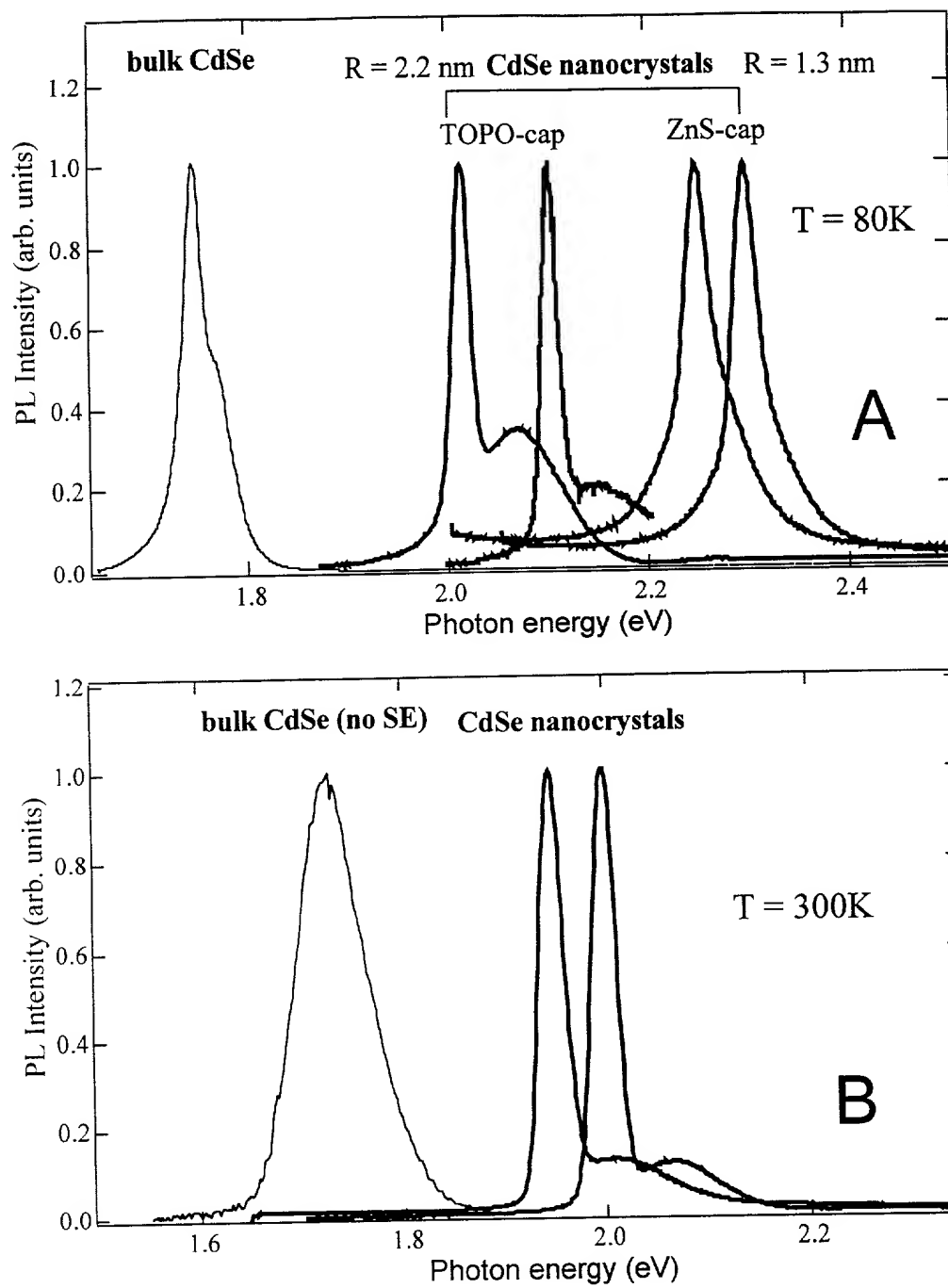


FIG. 9